

NXPLOS-I Series PLDRO: Integrated Internal Reference, Phase Locked DRO (PLDRO) - Typical Specifications

Frequency Range in GHz	0.5 - 2.8	2.8 - 9.0	9.0 - 14.0	14.0 - 16.5	16.5 - 23.0
Output Power in dBm (over temp)	+12	+12	+11	+10	+8
Frequency Stability (over temp, & over 3 yr.)	< +/- 5 ppm				
Frequency Accuracy in ppm @ 25 degC	+/- 1				
Power variation in dB (over temp)	< 2.0	< 2.0	< 3.0	< 3.0	< 3.0
Pulling (1.5:1 VSWR)	Will not break lock				
Harmonics in dBc (typ)	-15	-15	-20	-25	-25
Subharmonics in dBc (typ)					-45
Discrete Spurious in dBc (Fo<+/- 300 MHz) (1)	-75	-75	-75	-75	-70
Discrete Spurious in dBc (Fo>+/- 300 MHz) (1)	-65	-65	-65	-65	-65
Spurious plots (in GHz)			9.125		
Typical Phase Noise @ 100 Hz offset	-73	-73	-70	-67	-65
@ 1KHz offset	-99	-99	-93	-90	-87
@ 10KHz offset	-115	-115	-107	-102	-99
@ 100KHz offset	-117	-117	-110	-105	-102
@ 1MHz offset	-125	-125	-123	-120	-115
Phase Lock Alarm	Open Collector, locked open, unlocked ground				
Phase Voltage Monitor	1 - 11V				
Operating Temperature (base plate)	0 to +60 deg C (Military temperature range available)				
Power Supply (2)	+12 +/- 3% VDC, 400 mA max, 750 mA surge				
RF Connector	SMA Female (Field replaceable option available)				
DC Connector	Solder pin				
Size Length X Width (inches)	2.25 X 2.25				
Size: Height (exclude tuning screw [.25" max]) (inches)	1.48	1.61	1.48	1.48	1.48
Outline: DC200106	Rev. 2B	Rev. 2F	Rev. 2B		
Weight (in ounces):	6.3				
Other outline options:	Dual Output DC200106 Rev.2D				
Notes: (1) Lower spurious option (<-90 dBc) available					
(2) Other supply voltage available					
*Please note: Guaranteed phase noise will be 5 dB higher than typical. Better phase noise available.					
Phase noise plots (in GHz)		5.14			18.26666